

IN THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) An apparatus, including:

an amplifier to produce an output signal and to receive an input signal including an adjustable phase to be adjusted in response to an indication of an amplitude of the output signal to reduce a phase distortion; and

a translation circuit to transform the indication of the amplitude into a control signal to adjust the adjustable phase, wherein the translation circuit is to provide a loop gain of less than about one.

2. (Original) The apparatus of claim 1, further including:

an envelope detector to detect the indication of the amplitude.

3. (Original) The apparatus of claim 1, further including:

a varactor, wherein the adjustable phase is to be adjusted by translating the indication of the amplitude into a control signal to control the varactor.

4. (Canceled)

5. (Canceled)

6. (Currently Amended) The apparatus of claim 1[[4]], wherein the translation circuit includes at least one of an offset circuit, a gain circuit, and/or a law conformance circuit.

7. (Original) The apparatus of claim 1, wherein at least a portion of the amplifier includes complementary metal oxide semiconductor (CMOS) technology.

8. (Original) An apparatus, including:
 - a first stage including a first amplifier responsive to a first input signal; and
 - a second stage coupled to the first stage, the second stage including a second amplifier responsive to a second input signal, wherein the second input signal includes an adjustable phase to be adjusted in response to an indication of an amplitude of an output signal to reduce a phase distortion, and wherein the first input signal includes an adjustable amplitude to be adjusted to reduce an amplitude distortion.
9. (Original) The apparatus of claim 8, wherein the second stage is to provide the output signal.
10. (Original) The apparatus of claim 8, further including:
 - a third stage including a third amplifier to provide the output signal, wherein the third stage is coupled to the second stage.
11. (Currently Amended) A system, including:
 - an amplifier to produce an output signal and to receive an input signal including an adjustable phase to be adjusted in response to an indication of an amplitude of the output signal to reduce a phase distortion of the amplifier, wherein the adjustable phase is capable of being adjusted while leaving a signal amplitude associated with the amplifier substantially unchanged; and
 - an omnidirectional antenna coupled to the amplifier.
12. (Original) The system of claim 11, further including:
 - a translation circuit to transform the indication of the amplitude into a control signal to adjust the adjustable phase.
13. (Original) The system of claim 12, further including:

a tuning element selected from a capacitor and an inductor, the tuning element to receive the control signal to adjust the adjustable phase.

14. (Canceled)

15. (Original) The system of claim 11, wherein the phase distortion of the amplifier is capable of being reduced while a power output of the amplifier is increased from a first selected level to a second selected level.

16. (Currently Amended) A method, including:

detecting an indication of an amplitude of an output signal of an amplifier; and
adjusting a phase of an input signal of the amplifier responsive to the indication to reduce a change in a phase of the output signal, wherein the indication is transformed into a control signal, and wherein a tuning element selected from a capacitor and an inductor is used to receive the control signal to adjust the adjustable phase.

17. (Original) The method of claim 16, wherein detecting the indication of the amplitude of the output signal further includes:

detecting an envelope of the amplitude of the output signal.

18. (Original) The method of claim 16, wherein detecting the indication of the amplitude of the output signal further includes:

detecting a peak value of the amplitude of the output signal.

19. (Original) The method of claim 16, wherein the indication of the amplitude of the output signal includes an output signal power value.

20. (Original) The method of claim 16, wherein adjusting the phase of the input signal further includes:

reducing the change in the phase of the output signal.

21. (Original) The method of claim 16, wherein adjusting the phase of the input signal further includes:
reducing a change in the amplitude of the output signal.

22. – 25. (Canceled)

26. (Original) An apparatus, including:
a first stage including a first amplifier responsive to a first input signal;
a second stage coupled to the first stage, the second stage including a second amplifier responsive to a second input signal, wherein the second input signal includes an adjustable phase to be adjusted in response to an indication of an amplitude of an output signal to reduce a phase distortion, and wherein the first input signal includes an adjustable amplitude to be adjusted to reduce an amplitude distortion;
a translinear circuit to be coupled to the second input signal and to the indication, and to adjust the adjustable phase; and
a third stage including a third amplifier to provide the output signal, wherein the third stage is coupled to the second stage.

27. (Original) The apparatus of claim 26, wherein the translinear circuit is to approximate a mathematical function.

28. (Original) The apparatus of claim 26, further including:
a varactor to couple the translinear circuit to the second input signal.